

## FNK N-Channel Enhancement Mode Power MOSFET

### **Description**

The FNK06e uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications .It is ESD protested.

#### **General Features**

● V<sub>DS</sub> = 20V,I<sub>D</sub>=6A

 $R_{DS(ON)} < 26 \,\mathrm{m}\Omega$  @  $V_{GS} = 2.5 \mathrm{V}$ 

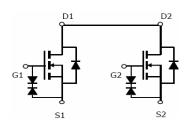
 $R_{DS(ON)} <_{22} m\Omega @ V_{GS} = 4.5V$ 

ESD Rating: 2000V HBM

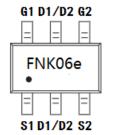
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

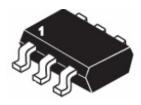
## **Application**

- ●PWM application
- ●Load switch



Schematic diagram





Marking and pin Assignment

SOT23-6L top view

#### **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
FNK06e	FNK06e	SOT23-6	Ø180mm	8mm	3000 units

#### Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous	I <sub>D</sub>	6	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	24	Α
Maximum Power Dissipation	P <sub>D</sub>	1.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	83.3	°C/W
--	----------------	------	------

### Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V	-	-	1	μΑ



# FNK06e

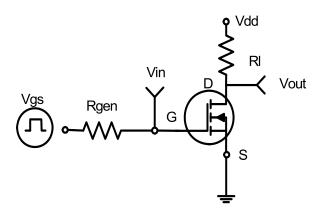
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	±10	μΑ
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.55	0.7	0.95	V
Dunin Course On State Besistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6.5A	-	15	22	mΩ
Drain-Source On-State Resistance		V <sub>GS</sub> =2.5V, I <sub>D</sub> =5.5A	-	20	26	mΩ
Forward Transconductance	<b>g</b> Fs	V <sub>DS</sub> =5V,I <sub>D</sub> =7A	-	20	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -40\/\/ -0\/	-	1120	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =10V, $V_{GS}$ =0V, F=1.0MHz	-	170	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F-1.0IVID2	-	135	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	6		nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =10V,R <sub>L</sub> =1.35Ω	-	13		nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =5V, $R_{GEN}$ =3 $\Omega$	-	52		nS
Turn-Off Fall Time	t <sub>f</sub>		_	16		nS
Total Gate Charge	Qg	\/ -10\/   -74	-	15		nC
Gate-Source Charge	$Q_{\mathrm{gs}}$	$V_{DS}$ =10V, $I_{D}$ =7A, $V_{GS}$ =4.5V	_	0.8	-	nC
Gate-Drain Charge	$Q_{gd}$	v <sub>GS</sub> =4.5v	-	3.2	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	7	Α

## Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production



### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



**Figure 1:Switching Test Circuit** 

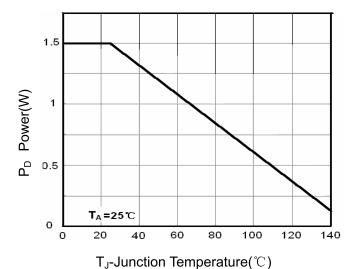


Figure 3 Power Dissipation

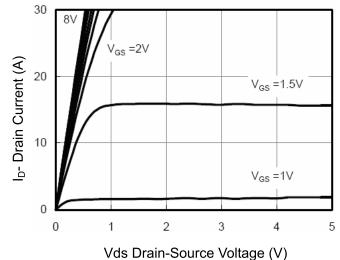


Figure 5 Output CHARACTERISTICS

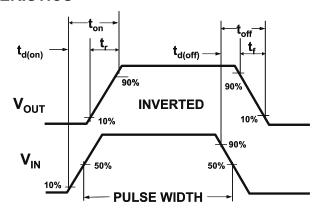


Figure 2:Switching Waveforms

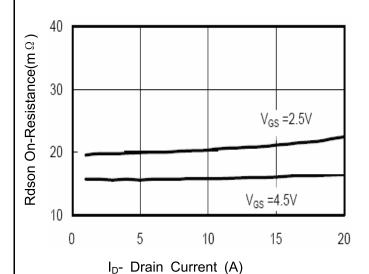


Figure 6 Drain-Source On-Resistance

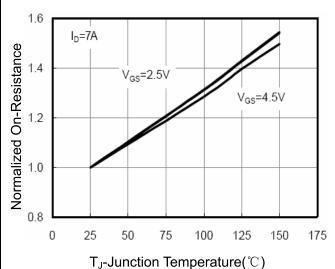
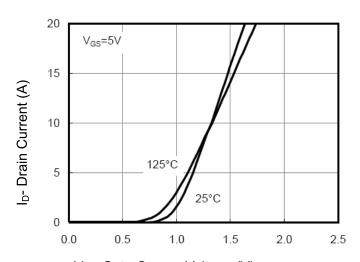


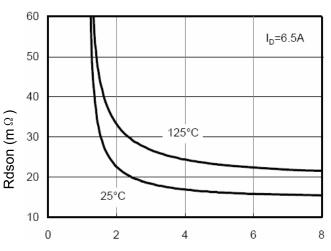
Figure 8 Drain-Source On-Resistance





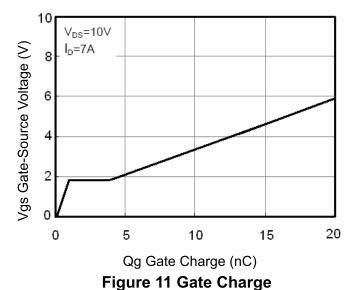
Vgs Gate-Source Voltage (V)

**Figure 7 Transfer Characteristics** 



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs



2000 1600 C<sub>iss</sub>

C<sub>oss</sub>

0

5
10
15
20

Vds Drain-Source Voltage (V)

Figure 8 Capacitance vs Vds

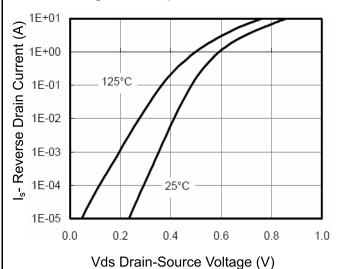


Figure 10 Capacitance vs Vds

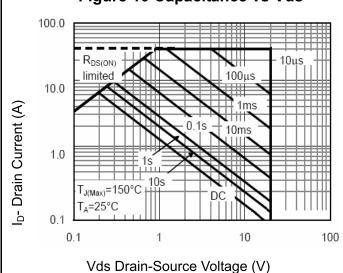
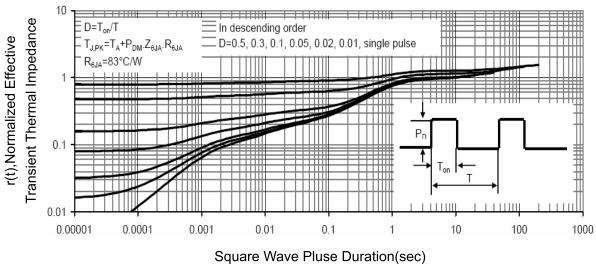


Figure 13 Safe Operation Area



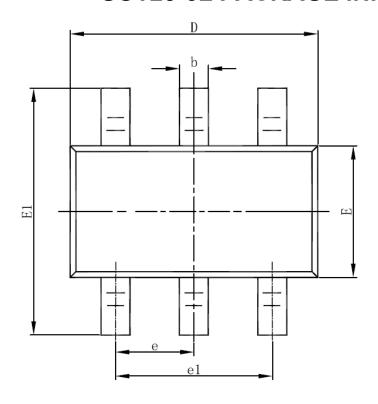


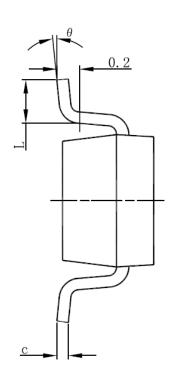
**Figure 14 Normalized Maximum Transient Thermal Impedance** 

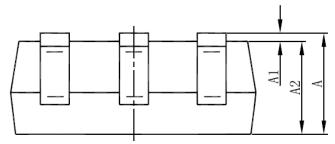
FNK-Semiconductor 5/7 Rev.1.1



## **SOT23-6L PACKAGE INFORMATION**







Cls a l	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037	7(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	



#### ATTENTION:

- FNK reserves the right to make changes to the information herein for the improvement of the design and performance without further notice! Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current.
- FNK assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all FNK products described or contained herein.
- Specifications of any and all FNK products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- FNKstrives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all FNK products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the FNK product that you intend to use.
- FNK will supply the best possible product for customers!

FNK-Semiconductor 7/7 Rev.1.1