

FNK N-Channel Enhancement Mode Power MOSFET

Description

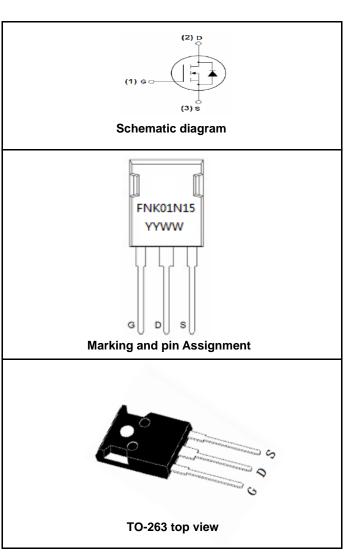
The FNK01N15T uses advanced trench Technology and design to provide excellent $R_{DS(ON)}$ with low gate charge .It can be used in a wide variety of applications

General Features

- V_{DS} = 100V,I_D =150A
 R_{DS(ON)} <5.7mΩ @ V_{GS}=10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation
- Good stability and uniformity with high EAS

Application

- Power switching application
- Hard switched and high frequency circuits
- •Uninterruptible power supply



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
FNK01N15T	FNK01N15T	TO-247	-	-	-

ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	100	V
Gate-Source Voltage	Vgs	±20	V
Drein Current Centinueus @ Current Buland (Nets 1)	l₀(25℃)	150	А
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	600	А
Maximum Power Dissipation	PD	375	W
Single pulse avalanche energy(Note 5)	EAS	433	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C



THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	0.4	°C /W
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS	I		-	r		
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V I_D=250\mu A$	100	110		V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
ON CHARACTERISTICS (Note 3)	U		-1	1		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	2.8	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A		5.0	5.7	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =20A				S
DYNAMIC CHARACTERISTICS (Not	e4)					
Input Capacitance	C _{lss}			6470		PF
Output Capacitance	Coss	V _{DS} =50V,V _{GS} =0V, F=1.0MHz		690		PF
Reverse Transfer Capacitance	C _{rss}	- F=1.0WHZ		430		PF
SWITCHING CHARACTERISTICS (N	ote 4)				•	
Delay Time	t _{d(on)}			28		nS
Turn-on Rise Time	tr	V_{DD} =50V, V_{GS} =10V, R_{GEN} =3 Ω		22		nS
Turn-Off Delay Time	t _{d(off)}	I _D =2A RL=2.5Ω		43.5		nS
Turn-Off Fall Time	t _f			14.5		nS
Total Gate Charge	Qg			139		nC
Gate-Source Charge	Q _{gs}	V _{DS} =50V,I _D =20A,V _{GS} =10V		34		nC
Gate-Drain Charge	Q _{gd}			56		nC
DRAIN-SOURCE DIODE CHARACTE	RISTICS		•		•	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A		0.85	1.2	V
Diode Forward Current (Note 2)	ls				160	А
Reverse Recovery Time	trr	TJ = 25℃, IF = 20A		60	90	nS
Reverse Recovery Charge	Qrr	di/dt = 500A/us(Note3)		177	200	nC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

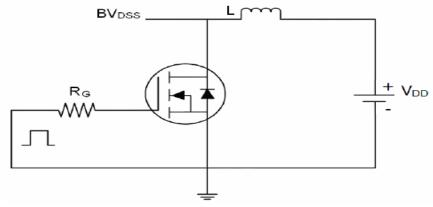
2. Surface Mounted on $1in^2$ FR4 Board, t ≤ 10 sec.

- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production testing
- 5.EAS condation:Tj=25 $^\circ C$,Vdd=20V.Vg=10V,L=0.5mH,Rg=25 Ω

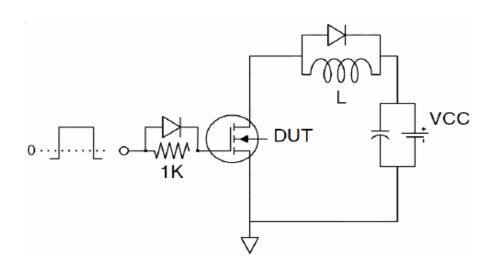


Test circuit

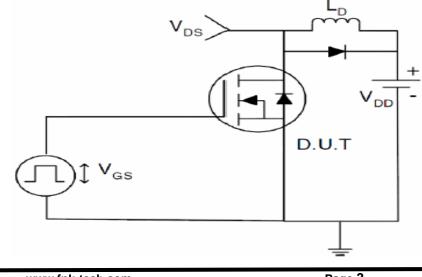
1) EAS Test Circuit



2) Gate Charge Test Circuit

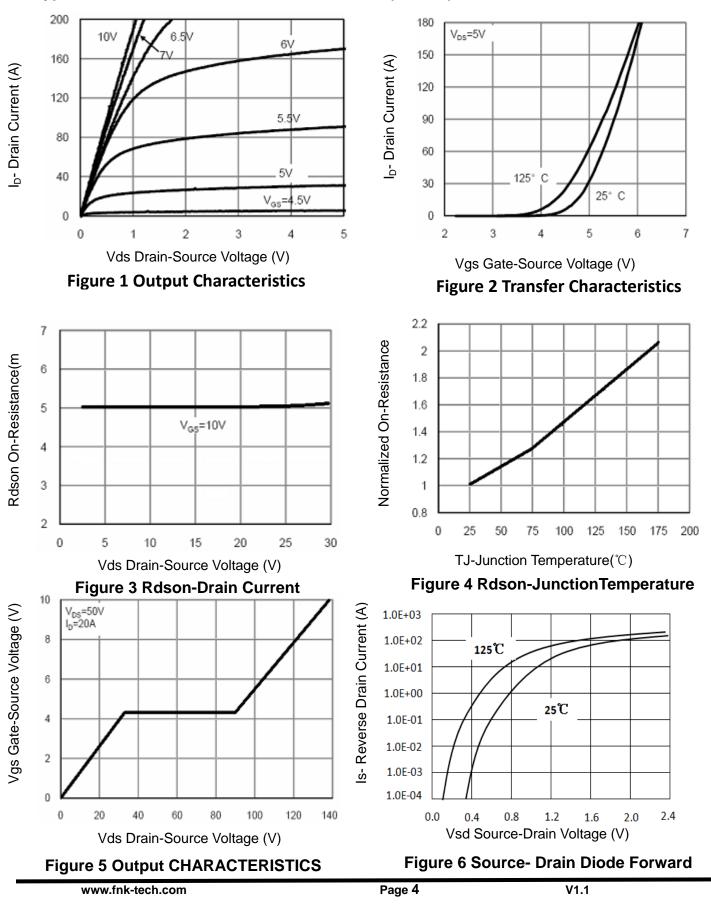


3) Switch Time Test Circuit

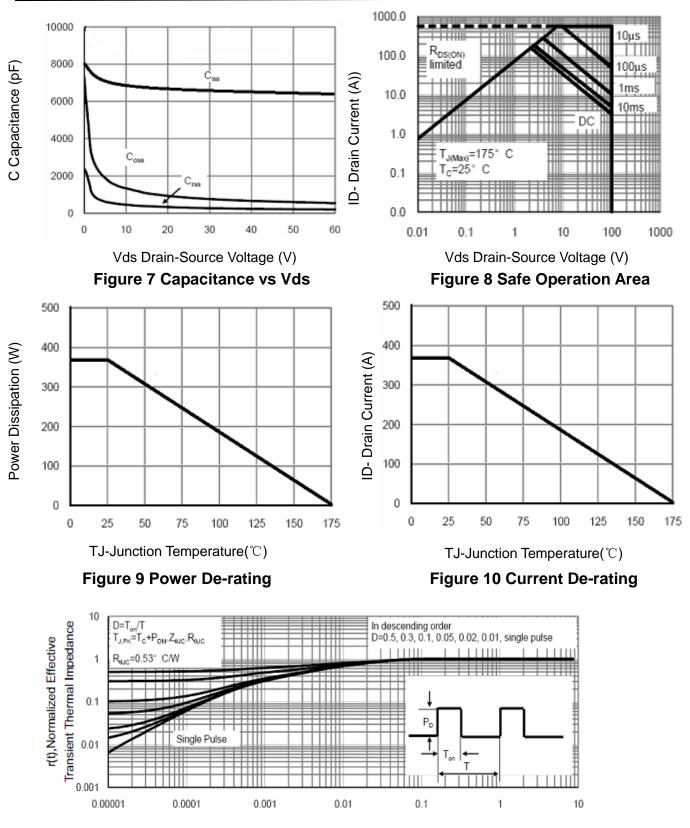




Typical Electrical and Thermal Characteristics (Curves)



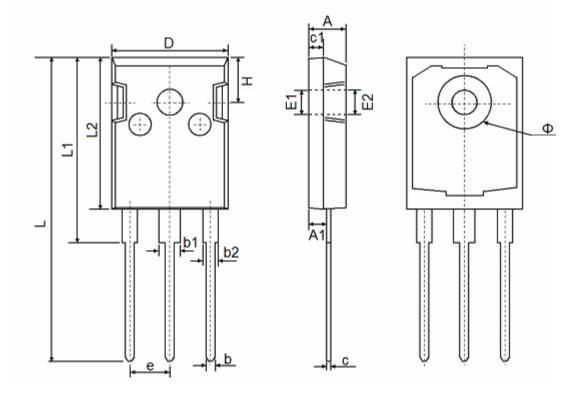




Square Wave Pluse Duration(sec) Figure 11 Normalized Maximum Transient Thermal Impedance







Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	4.850	5.150	0.191	0.200	
A1	2.200	2.600	0.087	0.102	
b	1.000	1.400	0.039	0.055	
b1	2.800	3.200	0.110	0.126	
b2	1.800	2.200	0.071	0.087	
с	0.500	0.700	0.020	0.028	
c1	1.900	2.100	0.075	0.083	
D	15.450	15.750	0.608	0.620	
E1	3.500 REF 0.138 R		REF		
E2	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626	
L1	24.800	25.100	0.976	0.988	
L2	20.300	20.600	0.799	0.811	
Φ	7.100	7.300	0.280	0.287	
e	5.450 TYP		0.215 TYP		
Н	5.98	REF	0.235 REF		



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